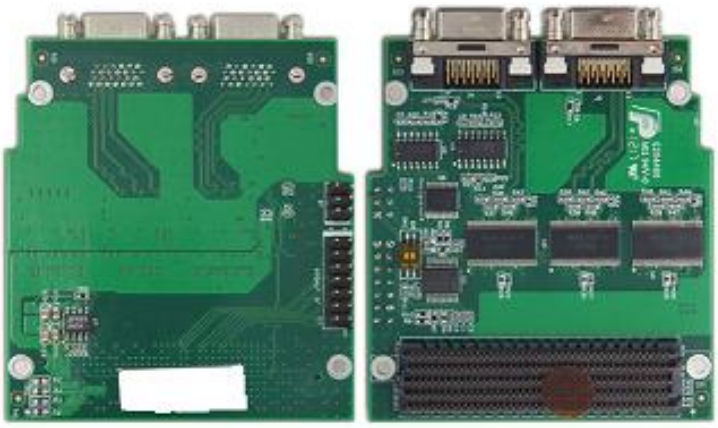
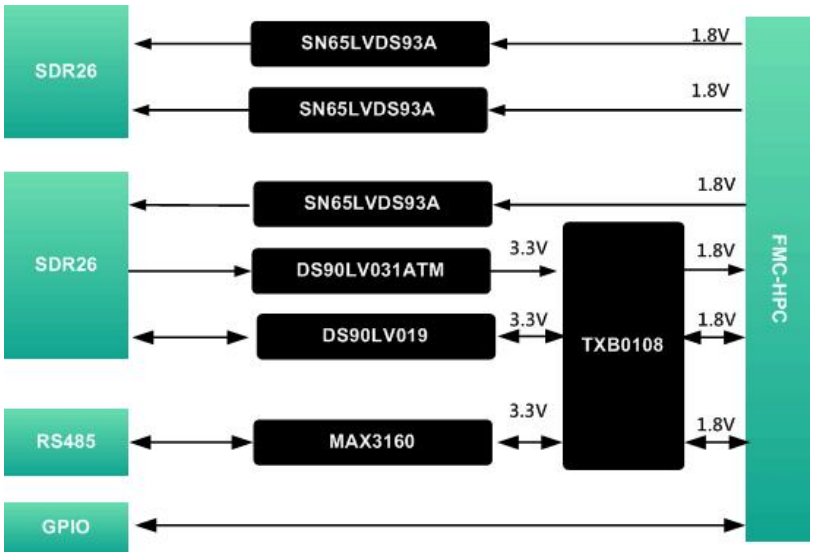
FMC based on Full Camera Link output card compatible with 1.8V IO



1. Overview

The Full Camera Link output sub-card based on FMC compatible with 1.8V IO supports Base, Middle and Full Camera Link signal output, and is compatible with 1.8V, 2.5V and 3.3V IO FPGA signal output. Adapt xilinx development boards of different models and FMC boards within the company. Board card is suitable for professional image application, analog source output and so on.



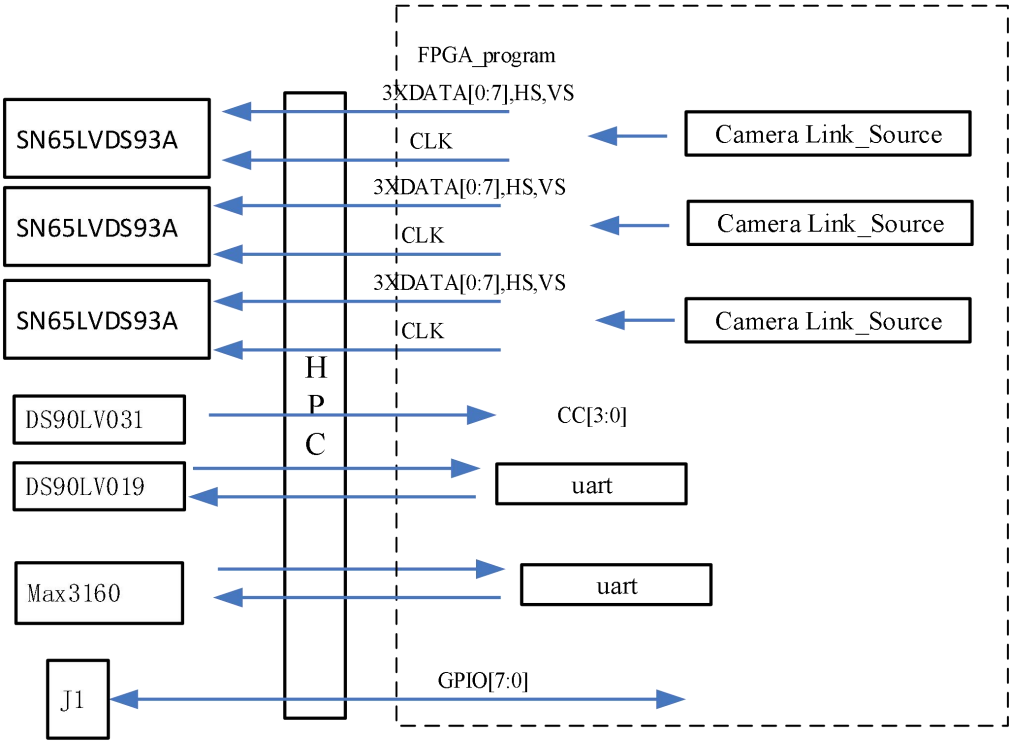
2. Specification

Camera link Output Parameters	Contents
Chip type	SN65LVDS93A
Channel amount	One channel base on Middleor Full One channel uart;Four channel cc

Clocking rate	85Mbps
Data bits wide	Maximum 8Byte
Transfer impedance	The differential100Ω
Connector	Two channel SDR26
FMC signal IO type	LVTTL
FMC signal Electrical level	1.8V,2.5V,3.3V
IO	8
RS485/RS232	One channel
connector	Pin board
FMC-HPC	ASP-134488-01
Front board	Two channel SDR26
Standard	FMC ANSI/VITA 57.1 - 2008
Size	69X76.5mm;The mounting hole size: 2.7mm
Power supply	+3.3V@0.5A
Power consumption	2W
Working temperature	Industrial -20℃ to +70℃
Xilinx board	V6、V7、KU、VU、 ZYNQ、ZU development board
Orihard board	136、270、367、288、330、274、3、9

3. Software

The board card provides the analog Camera Link signal output interface program corresponding to FPGA board, including Camera Link, uart, CC signals; External GPIO, RS232 test signals.



4. Application

Industrial image output, image analog source